### **Tutorial**

# **Integrating Verification and Testing** of Object-Oriented Software

Christian Engel Christoph Gladisch Vladimir Klebanov Philipp Rümmer

www.key-project.org

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Integrating Verification and Testing:

# **KeY Project Partners**



University of Koblenz Bernhard Beckert



University of Karlsruhe Peter H. Schmitt



Chalmers University Reiner Hähnle



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# Part I

### Introduction



Integrating Verification and Testing:

### What is this Tutorial about?

- Design & formal specification
- Deductive verification
- Testing

of

Object-oriented software



This tutorial has been developed in the KeY project. The demos will use the KeY tool.





### Some Buzzwords about KeY

- Java Card as target language
- Integration with two standard SWE tools:
  - Borland Together, a commercial CASE tool
  - Eclipse, an open extensible IDE
- Specification languages
  - JML
  - UML/OCL
- Dynamic logic as program logic
- Verification = symbolic execution + induction
- Sequent style calculus + meta variables + incremental closure
- Interactive/automated prover with advanced UI



Integrating Verification and Testing: Topic of the Tutorial

# **Verifying Java Card Programs**

### What is Java Card?

- Sun's standard for smart cards and embedded devices
- Subset of Java, but with transaction concept



### **KeY Works With**

### In this tutorial:

100% Java Card

### Other rule bases:

- ODL, a minimal abstract object oriented language
- A subset of the C language
- ASM, Abstract State Machines [Stanislas Nachen, ETH Zürich]
- HyKeY, differential dynamic logic for hybrid systems

[André Platzer, Univ. of Oldenburg]





Integrating Verification and Testing: Topic of the Tutorial

# **Verifying Java Card Programs**

### Why Java Card?

Good example for real-world object-oriented language

### Java Card Jacks

- garbage collection
- dynamical class loading
- multi-threading
- floating-point arithmetic

### Application areas are

- security critical
- prone to financial risk

# Formal Methods Integrated in KeY

### **Specification**

- UML + Object Constraint Language (OCL)
- Java Modeling Language (JML)

### Verification

- Dynamic Logic
- Decision procedures

### And ...

- Static analysis
- Test case generation

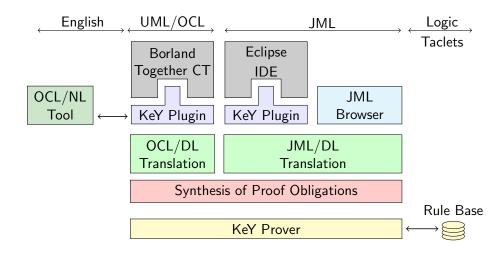


Integrating Verification and Testing: Architecture of the KeY Tool

# Part II

# **Specification**

### Architecture of the KeY Tool



Kβy

Integrating Verification and Testing: Architecture of the KeY Tool

### Part II

# **Specification**

- 3 Design by Contract
- **4** OCL Specification
- **5** JML Specification
- 6 Specification in Dynamic Logic (DL)
- **7** A Verification Example with JML

# **Design by Contract**

### Class

Invariant

### Operation

Precondition

Modifies Clauses

Postcondition

Termination, more precisely: normal or exceptional



Integrating Verification and Testing: Design by Contract

# **OCL: Object Constraint Language**

### **Object Constraint Language**

- Part of the OMG standard UML
- Present Version: 2.0
- Adds formal constraints to UML (class) diagrams
- Accessible to people without a strong mathematical background



Synthesis of Proof Obligations

Rule Base





### Part II

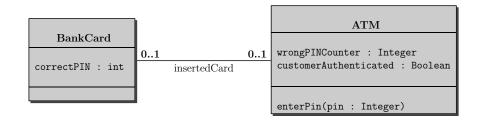
# **Specification**

- 3 Design by Contract
- **4** OCL Specification
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Integrating Verification and Testing: OCL Specification

# A Typical UML Diagram



# Design by Contract with OCL

Class

Invariant

Operation

Precondition

Postcondition

**Modifies Clauses** 

Termination



Integrating Verification and Testing: OCL Specification

# **Proof Obligations**

context D extends C context C

inv: T inv: J

### Behavioural Subtyping for classes

For all instances o of D : o.J implies o.I.

# Design by Contract with OCL

context ATM

inv: 0 <= self.wrongPinCounter and</pre> self.wrongPinCounter <= 2</pre>

context ATM::enterPin(pin: Integer)

pre: insertedCard <> null and not customerAuthenticated

and not pin = insertedCard.correctPIN

and wrongPINCounter < 2

post: wrongPINCounter = wrongPINCounter@pre + 1

and not customerAuthenticated

Modifies Clauses not explicitely supported by OCL

Termination specification not explicitely supported by OCL

Integrating Verification and Testing: OCL Specification

# **Proof Obligations**

context C::op1 context D::op1 pre: pre1 pre2 post: post2 post: post1

D extends C

# Behavioural Subtyping for operations

pre1 implies pre2 and post2 implies post1





# **Proof Obligations**

context C::op pre: pre

post: post

Implementation p of op.

### **Ensures Postcondition**

If p is started in a state satisfying pre then p terminates and in the final state post is true.



Integrating Verification and Testing: OCL Specification

### Part II

# **Specification**

- **JML Specification**
- A Verification Example with JML

# **Proof Obligations**

context C::op pre: pre

context C inv: I

post: post

Implementation p of op.

### Preserves Invariant

If p is started in a state satisfying pre and I then p terminates and in the final state I is again true.



Integrating Verification and Testing: OCL Specification

# JML: Java Modeling Language

### Java Modeling Language

- Behavioral interface specification language for Java
- International community effort
- More and more tools: Runtime checkers, static analysis, program verification

JML/FOL Translation

Synthesis of Proof Obligations

Rule Base

**KeY Prover** 





# **Design by Contract with JML (Invariants)**

```
public class ATM {
 /*@ private invariant
                        wrongPINCounter >= 0 &&
                        wrongPINCounter <= 2
   @*/
  private BankCard insertedCard
                                          = null:
  private boolean customerAuthenticated = false;
                   wrongPINCounter
  private int
                                          = 0;
  public void enterPIN (int pin) { ...
}
```

Integrating Verification and Testing: JML Specification

# **Proof Obligations**

### **JML Proof Obligations**

- Behavioural Subtyping for classes
- Behavioural Subtyping for operations
- Strong Operation Contract
- Ensures Postcondition
- Preservation of Invariants
- Correctness of Modifies Clauses

Synthesis of Proof Obligations Rule Base **KeY Prover** 

# **Design by Contract with JML (Operation Contracts)**

```
public class ATM {
/*@ public normal_behavior
  @ requires
               insertedCard != null;
  @ requires
              !customerAuthenticated;
              pin != insertedCard.correctPIN;
  @ requires
               wrongPINCounter < 2;</pre>
  @ requires
               wrongPINCounter ==
  @ ensures
                      \old(wrongPINCounter) + 1;
  @ assignable wrongPINCounter;
  @ also ...
  @*/
  public void enterPIN (int pin) { ...
```

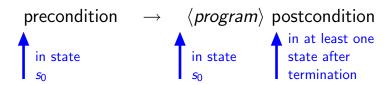
Integrating Verification and Testing: JML Specification

### Part II

# **Specification**

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### **Total Correctness Statement**





Integrating Verification and Testing: Specification in Dynamic Logic (DL)

### Part II

# **Specification**

- A Verification Example with JML

# **Specification in Dynamic Logic**

```
\forall ATM x0;
    x0.wrongPINCounter = ATM::wrongPINCounter@pre(x0) &
!self.insertedCard = null &
!self.customerAuthenticated = TRUE &
!pin = self.insertedCard.correctPIN &
self.wrongPINCounter < 2</pre>
\< self.enterPIN(_pin)@ATM;\> self.wrongPINCounter =
    ATM::wrongPINCounter@pre(self) + 1
                                                 Rule Base
```

KeY Prover



Integrating Verification and Testing: Specification in Dynamic Logic (DL)

# An Example Program swapMax()

```
public class Test {
   private int idx;
   /*@ requires precondition @ */
   /*@ ensures postcondition @ */
   void swapMax(int[] a) {
      int counter = -1; idx = 0;
   /*@ loop_invariant @*/
      while (++counter < a.length) {</pre>
         if (a[counter] > a[idx]) idx=counter;
      int tmp=a[idx]; a[idx]=a[0]; a[0]=tmp;
   }
}
```

# JML Specification of swapMax()

```
/*0 requires a!=null && a.length > 0;
  @ ensures
     (\forall int x; x==idx;
     \operatorname{old}(a[0]) == a[x] \&\& \operatorname{old}(a[x]) == a[0]) \&\&
       (\forall int i; 0 \le i \&\& i \le old(a.length);
       a[0] >= a[i] &&
          (i!=0 \&\& i!=idx ==> a[i]==\setminus old(a[i])):
    diverges false;
  0 */
    void swapMax(int[] a) { ... }
```

Integrating Verification and Testing: A Verification Example with JML

### Proving Postconditions for swapMax()

### After termination of the loop, we have...

\forall int i; 
$$((0 \le i \& i \le a.length) \rightarrow a[idx] \ge a[i])$$

### But we also need to show that executing...

```
tmp=a[idx]; a[idx]=a[0]; a[0]=tmp;
gives us
       \forall int i; ((0 \le i \& i \le a.length \& i \ne 0 \& i \ne idx) \rightarrow
                       a[i] = \setminus old(a[i]))
```

### So...

Loop invariant needs to be strengthened!

### **JML Loop Invariant**

```
/*@ loop_invariant
      -1 <= counter && counter <= a.length &&
                  && idx < a.length
 @ (\forall int x; x>=0 && x<=counter;</pre>
          a[idx] >= a[x];
 @ decreases (a.length - counter);
  @*/
 while (++counter < a.length) {</pre>
       if (a[counter] > a[idx])
         idx=counter;}
```

Integrating Verification and Testing: A Verification Example with JML

# **Improved JML Loop Invariant**

```
/*@ loop_invariant
 0 -1<=counter && counter<=a.length &&
  0 <= i dx
                 && idx < a.length
 @ (\forall int x; x>=0 && x<=counter;</pre>
          a[idx] >= a[x]);
  @ decreases (a.length - counter);
 @ assignable idx, counter;
  @*/
  while (++counter < a.length) {</pre>
       if (a[counter] > a[idx])
         idx=counter:}
```

# Part III

# **Logic and Calculus**



- 9 Sequent Calculus
- 10 Rules for Programs: Symbolic Execution
- **11** A Calculus for 100% Java Card
- Interactive and Automated Proof Construction



Integrating Verification and Testing: Java Card DL

# Why Dynamic Logic?

- Transparency wrt target programming language
- More expressive and flexible than Hoare logic
- Can use reference implementations instead of first-order theories
- Symbolic execution is a natural interactive proof paradigm
- Proven technology that scales up

### Part III

# **Logic and Calculus**



Integrating Verification and Testing:

# **Syntax and Semantics**

### Syntax

- Basis: Typed first-order predicate logic
- Modal operators  $\langle p \rangle$  and [p] for each (Java Card) program p
- Class definitions in background (not shown in formulas)

### **Semantics**

- Operators refer to the final state of p
- [p] F: If p terminates, then F holds in the final state (partial correctness)
- F: p terminates and F holds in the final state

  (total correctness)

Java Card DL formulas contain unaltered Java Card source code

# Why Dynamic Logic?

- Transparency wrt target programming language
- More expressive and flexible than Hoare logic
- Can use reference implementations instead of first-order theories
- Symbolic execution is a natural interactive proof paradigm
- Proven technology that scales up
- Programs are "first-class citizens"
- No encoding of program syntax nor semantics into logic
- Rule for each program construct in calculus



Integrating Verification and Testing: Java Card DL

# Why Dynamic Logic?

- Transparency wrt target programming language
- More expressive and flexible than Hoare logic
- Can use reference implementations instead of first-order theories
- Symbolic execution is a natural interactive proof paradigm
- Proven technology that scales up

Class initialization much easier to specify with code

# Why Dynamic Logic?

- Transparency wrt target programming language
- More expressive and flexible than Hoare logic
- Can use reference implementations instead of first-order theories
- Symbolic execution is a natural interactive proof paradigm
- Proven technology that scales up

Not merely partial/total correctness:

- Correctness of program transformations
- Security properties
- Temporal extensions



Integrating Verification and Testing: Java Card DL

# First-Order Formula Syntax

ASCII syntax, keywords preceded by '\'

### Logical operators

& and

or

implication

equivalence

! negation

### **Logical constants**

true

false

### **Conditional terms**

 $\inf(\ldots) \operatorname{then}(\ldots) \operatorname{else}(\ldots)$ 

### Quantifiers

\forall

\exists

# **Dynamic Logic Example Formulas**

 $(\texttt{balance} > 1 \; \& \; \texttt{amount} > 1) \; -\!\!\!> \; \texttt{<charge(amount);>} \\ (\texttt{balance} > 1)$ 

<x = 1;>([while (true) {}] false)

Syntax? ok

Program formulas can appear nested



Integrating Verification and Testing: Java Card DL

### **Variables**

- Logical variables disjoint from program variables
  - No quantification over program variables
  - Programs do not contain logical variables
  - "Program variables" actually non-rigid functions

\exists int x; ([x = 1;](x = 1))

Syntax? bad

- x cannot be a logical variable, because it occurs in the program
- x cannot be a program variable, because it is quantified

<int x;>\forall int val; ((<p>x = val) <-> (<q>x = val)) Syntax? ok

• p, q equivalent relative to computation state restricted to x

### ΚĠ>

# **Type System**

### Static types

- Partially ordered finite type hierarchy
- Terms are statically typed (like Java expressions)
- Type casts in logic

### Dynamic types

- Each term value has a dynamic type
- Dynamic type depends on state
- Dynamic types conform to static types
- Type predicates in logic



Integrating Verification and Testing: Java Card DL

# **Rigid and Flexible Terms**

### Example

 $\langle int i; \rangle \langle int x; (i+1=x->\langle i++; \rangle (i=x))$ 

- Interpretation of i depends on computation state
- Interpretation of x and + must not depend on state

Locations are always flexible
Logical variables, standard functions are always rigid

⇒ flexible

 $\Rightarrow$  rigid

# **Semantics**

### Kripke semantics

- Semantics of a Java program is a partial function from states to states
- F true in state s iff p terminates and F holds in the final state s'
- A Java Card DL formula is valid iff it is true in all states

We need a calculus for checking validity of formulae



Integrating Verification and Testing: Java Card DL

# **Sequents and their Semantics**

### Syntax

$$\psi_1, \dots, \psi_m \implies \phi_1, \dots, \phi_n$$
Antecedent

where the  $\phi_i, \psi_i$  are formulae (without free variables)

### **Semantics**

Same as the formula

$$(\psi_1 \& \cdots \& \psi_m) \longrightarrow (\phi_1 \mid \cdots \mid \phi_n)$$

### Part III

# **Logic and Calculus**

- **8** Java Card DL
- Sequent Calculus
- Rules for Programs: Symbolic Execution
- A Calculus for 100% Java Card
- Interactive and Automated Proof Construction



Integrating Verification and Testing: Sequent Calculus

# **Sequent Rules**

### General form

RULE NAME 
$$\frac{\Gamma_1 ==> \Delta_1 \quad \cdots \quad \Gamma_r ==> \Delta_r}{\Gamma ==> \Delta}$$
Conclusion

(r = 0 possible)

### Soundness

If all premisses are valid, then the conclusion is valid

# Some Simple Sequent Rules

NOT\_LEFT 
$$\frac{\Gamma \Rightarrow A, \Delta}{\Gamma, !A \Rightarrow \Delta}$$

$$_{\text{IMP\_LEFT}} \ \frac{\Gamma \Rightarrow A, \ \Delta \qquad \Gamma, \ B \Rightarrow \Delta}{\Gamma, \ A \Rightarrow B \Rightarrow \Delta}$$

CLOSE\_GOAL 
$$\overline{\Gamma, A \Rightarrow A, \Delta}$$
 CLOSE\_BY\_TRUE  $\overline{\Gamma \Rightarrow \text{true}, \Delta}$ 

$$\frac{\Gamma, \text{ } \text{forall } t \text{ } x; \phi, \text{ } \{x/e\}\phi \Longrightarrow \Delta}{\Gamma, \text{ } \text{forall } t \text{ } x; \phi \Longrightarrow \Delta}$$

where *e* var-free term of type  $t' \prec t$ 



Integrating Verification and Testing: Sequent Calculus

### Part III

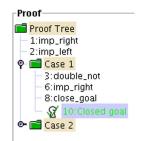
# **Logic and Calculus**

- 10 Rules for Programs: Symbolic Execution
- A Calculus for 100% Java Card
- **Interactive and Automated Proof Construction**

# **Sequent Calculus Proofs**

### Proof tree

- Proof is tree structure with goal sequent as root
- Rules are applied from conclusion (old goal) to premisses (new goals)
- Rule with no premiss closes proof branch
- Proof is finished when all goals are closed



Integrating Verification and Testing: Sequent Calculus

# **Proof by Symbolic Program Execution**

- Sequent rules for program formulas?
- What corresponds to top-level connective in a program?

### The Active Statement in a Program

### Example

$$\underbrace{1:\{\text{try}\{\atop \pi} \text{ i=0; } \underbrace{\text{j=0; } }_{\omega} \text{ finally}\{\text{ k=0; }\}\}$$

```
active statement i=0;
non-active prefix \pi
rest
```

# **Proof by Symbolic Program Execution**

- ullet Sequent rules execute symbolically the active (=  $1^{
  m st}$ ) statement
- Sequent proof corresponds to symbolic program execution

### Example: The rule for if-then-else (SIMPLIFIED VERSION!)

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Integrating Verification and Testing: Symbolic Execution

### **Problems to Address**

### Object attributes & arrays

Modelled as non-rigid functions

### Side effects

Expressions in programs can have side effects

### Example

if 
$$((y=3) + y < 0) \{...\}$$
 else  $\{...\}$ 

### **Aliasing**

Different names may refer to the same location

### Example

# Integrating Verification and Testing: A Calculus for 100% Java Card

### Part III

# **Logic and Calculus**

- **8** Java Card DL
- 9 Sequent Calculus
- Rules for Programs: Symbolic Execution
- 1 A Calculus for 100% Java Card
- **12** Interactive and Automated Proof Construction



Integrating Verification and Testing: A Calculus for 100% Java Card

### Other Issues

## Further supported Java Card features

- method invocation with polymorphism/dynamic binding
- arrays
- abrupt termination
- throwing of NullPointerExceptions, etc.
- object creation and initialisation
- bounded integer data types
- transactions

All Java Card language features are fully addressed in KeY

# Java—A Language of Many Features

# Java—A Language of Many Features

### Ways to deal

- Program transformation, up-front
- Local program transformation, done by a rule on-the-fly
- Modeling with first-order formulas
- Special-purpose constructs in program logic

Pro: Feature needs not be handled in calculus

Contra: Modified source code

Example in KeY: Very rare: treating inner classes



Integrating Verification and Testing: A Calculus for 100% Java Card

# Java—A Language of Many Features

### Ways to deal

- Program transformation, up-front
- Local program transformation, done by a rule on-the-fly
- Modeling with first-order formulas
- Special-purpose constructs in program logic

Pro: No logic extensions required, enough to express most features Contra: Creates difficult first-order POs, unreadable antecedents Example in KeY: Dynamic types and branch predicates

### Ways to deal

- Program transformation, up-front
- Local program transformation, done by a rule on-the-fly
- Modeling with first-order formulas
- Special-purpose constructs in program logic

Pro: Flexible, easy to implement, usable

Contra: Not expressive enough for all features

Example in KeY: Complex expression eval, method inlining, etc., etc.



Integrating Verification and Testing: A Calculus for 100% Java Card

# Java—A Language of Many Features

### Ways to deal

- Program transformation, up-front
- Local program transformation, done by a rule on-the-fly
- Modeling with first-order formulas
- Special-purpose constructs in program logic

Pro: Arbitrarily expressive extensions possible Contra: Increases complexity of all rules

Example in KeY: Method frames, updates

# **Handling Side Effects**

### **Problem**

- Expressions may have side effects
- Terms in logic have to be side effect free

### **Example**

$$(y=3) + y < 0$$

does not only evaluate to a boolean value, but also assigns a value to y



Integrating Verification and Testing: A Calculus for 100% Java Card

# Handling Assignment: Explicit State Updates

### Problem

Because of aliasing, assignment cannot be handled as syntactic substitution

### Solution

State updates as explicit syntactic elements

### **Syntax**

$$\{loc := val\}\phi$$

where (roughly)

- loc is a program variable x, an attribute access o.a, or an array access a[i]
- val is same as val, a literal, or a logical variable

# **Handling Side Effects**

### Solution

- Calculus rules realise a stepwise symbolic evaluation (simple transformations)
- Restrict applicability of some rules (e.g., if-then-else)

### Example

```
if ((y=3) + y < 0) \{...\} else \{...\}
rewritten into
                = 3:
         val1 = y;
int
         val0 = val1 + y;
boolean guard = (val0 < 0);</pre>
if (guard) {...} else {...}
```



Integrating Verification and Testing: A Calculus for 100% Java Card

# **Assignment Rule in KeY**

$$\frac{\Gamma \Longrightarrow \{loc := val\} < \pi \ \omega > \phi, \ \Delta}{\Gamma \Longrightarrow \langle \pi \ loc = val; \ \omega > \phi, \ \Delta}$$

### **Advantages**

- no renaming required
- delayed proof branching

### Update simplification in KeY

KeY system has powerful mechanism for simplifying and applying updates

- eager simplification (also: parallel updates)
- lazy application

# **Handling Abrupt Termination**

Example: try-throw

- Abrupt termination handled by "simple" program transformations
- Changing control flow = rearranging program parts

### Example

TRY-THROW (exc simple)

$$\Gamma \Rightarrow \left\langle \begin{array}{l} \pi \text{ if (exc instanceof T)} \\ \{\text{try \{e=exc; r\} finally \{s\}\}} \right\rangle \phi \\ \text{else \{s throw exc\}; } \omega \end{array} \right\rangle$$
 
$$\Gamma \Rightarrow \langle \pi \text{ try\{throw exc; q\} catch(T e)\{r\} finally\{s\}; } \omega \rangle \phi$$

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Integrating Verification and Testing: A Calculus for 100% Java Card

### Part III

# **Logic and Calculus**

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# Components of the Calculus

- Non-program rules
  - first-order rules
  - rules for data-types
  - rules for modalities
  - the induction rule
- Rules for reducing/simplifying the program (symbolic execution) Replace the program by combination of
  - case distinctions (proof branches) and
  - sequences of updates
- Rules for handling loops
  - rules using loop invariants
  - rules for handling loops by induction
- Rules for replacing a method invocations by the method's contract
- Update simplification



Integrating Verification and Testing: A Calculus for 100% Java Card

### Interaction and Automation

For realistic programs: Fully-automated verification impossible

### Interaction and Automation

### Goal in KeY: Integrate automated and interactive proving

- All easy or obvious proof steps should be automated
- Sequents presented to user should be simplified as far as possible
- Primary steps that require interaction: induction, treatment of loops
- Taclets enable interactive rule application mostly using mouse

# Typical workflow when proving in KeY (and other interactive provers)

- Prover runs automatically as far as possible
- When prover stops user investigates situation and gives hints (makes some interactive steps)
- **3** Go to 1



Integrating Verification and Testing: Proof Construction

# **Extension of Proof: Application of Single Taclets**

### Taclet application requires

- A proof goal
- Focus of rule application: term/formula in the goal
- Instantiation of schema variables

# Main procedure for applying a taclet interactively

- Select an application focus using mouse pointer
- 2 Select a particular rule from the context menu
- Instantiate schema variables

# Working with Sequents: Sequent View

### For goals (leaves of proof tree)

- Obtaining information about formulas/terms (press Alt key)
- Selecting formulas/terms, applying rules to them

### For inner nodes

 Inspecting parts involved in rule application (highlighted)

```
Current Goal

| self_ATM_lv_O.accountProxies@(ATM)[i_j = i_jml_lv_0)

self_ATM_lv_O.insertedCard@(ATM).accountNumbe

< 0,
self_ATM_lv_O.online@(ATM) = TRUE,
self_ATM_lv_O.insertedCard@(ATM).invalid@(Bank
= TRUE,
self_ATM_lv_O.inser
```

```
Inner Node
self_AIM_iv_U.centra!Host@(AIM).accounts@(Lentr
null,
self_AIM_iv_O.insertedCard@(AIM).invalid@(Bank(
= TRUE,
self_AIM_iv_O.enull,
self_AIM_iv_O.accountProxies@(AIM) = null,
self_AIM_iv_O.insertedCard@(AIM) = null,
self_AIM_iv_O.centra!Host@(AIM) = null,
self_AIM_iv_O.centra!Host@(AIM) = null,
\if (!self_AIM_iv_O.insertedCard@(AIM) = null)
\then (!pin:=pin_Iv_O.
self_AIM_iv_O.entra!Host@(AIM) = null)
\then (!pin:=pin_Iv_O.
self_AIM_iv_O.entra!Host@(AIM) = null)
```



Integrating Verification and Testing: Proof Construction

# **Applying Taclets using Drag-and-Drop**

### **Applying equations**

• Drag the equation onto the term to be rewritten

# Current Goal $a = b, c = b \Longrightarrow a = c$

### **Instantiating quantifiers**

Drag instantiation term onto the quantified formula

## 

**.** 

# Means of Automation Implemented in KeY

- Parameterized strategies for applying rules automatically
- Free-variable first-order calculus (non-destructive, proof-confluent)
- Invocation of external theorem provers, decision procedures:
  - Simplify (from ESC/Java)
  - ICS
  - Any other with SMT-LIB interface



Integrating Verification and Testing: Proof Construction

### Part IV

# **Integrating Testing and Verification**

# Strategies Currently Present in KeY

Strategies optimized for . . .

### Symbolic execution of programs

- Come in different flavours: with/without unwinding loops, etc.
- Concentrate on eliminating program and simplifying sequents

### Handling first-order logic

- Implements a complete first-order theorem prover
- Includes arithmetics solver



Integrating Verification and Testing: Proof Construction

### Part IV

# **Integrating Testing and Verification**

- Why Integrate?
- Test-Case Generation by Bounded Symbolic Execution
- Test-Case Generation from Method Specifications and Loop Invariants
- **10** White-box testing by Combining Specification Extraction and Black-box testing
- **Proving Incorrectness of Programs**

# Why Integrate?

### Testing makes sense, even in cases when a formal proof exists

- Testing can uncover bugs in environment (hardware, compiler, operating system, virtual machine)
- Testing can uncover bugs w.r.t. unspecified properties (e.g. timing)
- Tests are reusable after program changes

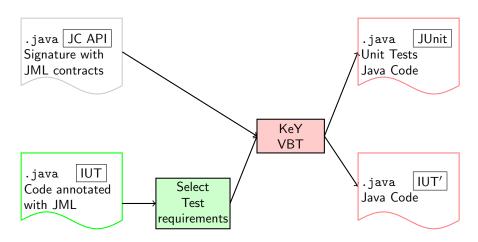
### Idea: Use a formal proof to generate test cases

- KeY provides the path condition for each execution path
- High code coverage (feasible execution paths)
- Tests can be generated from incomplete proofs

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Integrating Verification and Testing: Why Integrate?

### **Verification-Based Test Generation: Overview**



User input — Library — Automatically Generated

### Part IV

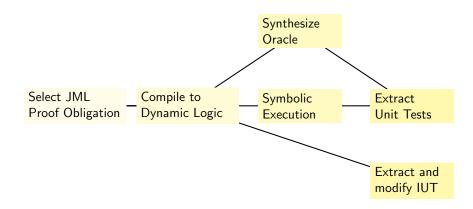
# **Integrating Testing and Verification**

- Why Integrate?
- Test-Case Generation by Bounded Symbolic Execution
- 15 Test-Case Generation from Method Specifications and Loop Invariants
- **10** White-box testing by Combining Specification Extraction and Black-box testing
- **Proving Incorrectness of Programs**

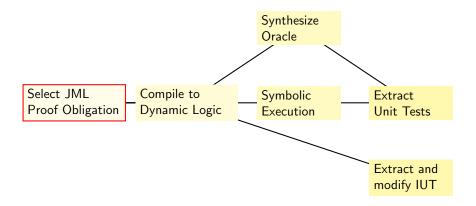
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Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Verification-Based Test Generation Process**



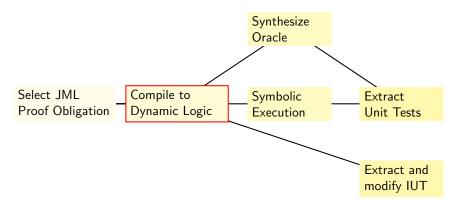
### **Verification-Based Test Generation Process**





Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Verification-Based Test Generation Process**



# **Example: Java Method with JML Specification**

```
public class Middle{
    /*@ public normal_behavior
    @ ensures \result==x || \result==y || \result==z;
    @ ensures ...
    @*/
    public static int middle(int x, int y, int z){
        int mid = z;
        ...
    }
}
```

 $K_{\mathcal{R}}$ 

Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### From Proof to Test

"Normalized" Proof obligation

$$\mathsf{Pre} \Rightarrow \mathcal{S} \mathsf{ Post}$$

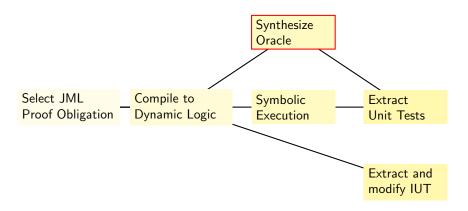
### Preparations

- Pre is a set of first-order formulas with preconditions, system invariant
- $\bullet$   $\ensuremath{\mathcal{S}}$  is initial (symbolic) state at start of execution of p
- ullet Extract IUT from  ${\cal S}$  and p,
- Synthesize test oracle from finitely guarded first-order formula Post





### **Verification-Based Test Generation Process**





Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

# **Synthesize Oracle**

### **Test oracle:**

```
((\_old0\_x = \_imlresult8)
          || (\_old1\_y = \_imlresult8 || \_old2\_z = \_imlresult8))
      && ( _{old1_y} >= _{jmlresult8} && _{old2_z} >= _{jmlresult8}
          || ( _{old0_x} >= _{imlresult8} \&\& _{old2_z} >= _{imlresult8}
             | | _{old0_x} > = _{jmlresult8}
             && _{old1_{y}} >= _{imlresult8})
      && ( _{old1_y} \leftarrow _{jmlresult8} && _{old2_z} \leftarrow _{jmlresult8}
          | | ( -old0_x \le -jmlresult8 \&\& -jmlresult8 >= -old2_z
             || _jmlresult8 >= _old0_x
             && _imlresult8 >= _old1_y)))
```

# **Synthesize Oracle**

### Postcondition obtained from proof tree:

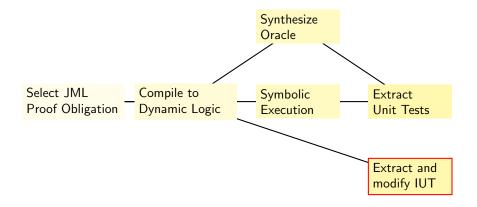
```
((\_old0\_x = \_imlresult8)
         | (\_old1\_y = \_jmlresult8 | \_old2\_z = \_jmlresult8))
      & ( _{old1_{-}y} >= _{imlresult8} & _{old2_{-}z} >= _{imlresult8}
         | ( \_old0_x > = \_jmlresult8 \& \_old2_z > = \_jmlresult8
             | -old0_x > = -imlresult8
             & _{old1_{y}} >= _{jmlresult8})
      & ( _{old1_{-}y} \leq _{-imlresult8} & _{old2_{-}z} \leq _{-imlresult8}
          | ( _old0_x <= _jmlresult8 & _jmlresult8 >= _old2_z
              | _imlresult8 >= _old0_x
             & _imlresult8 \geq _old1_v)))
```

Directly translatable to a boolean expression



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Verification-Based Test Generation Process**





# **Extract IUT – Preparations**

### State update S

```
{_old0_x:=x_lv_0 ||

_old1_y:=y_lv_0 ||

_old2_z:=z_lv_0 ||

x:=x_lv_0 ||

y:=y_lv_0 ||

z:=z_lv_0}
```

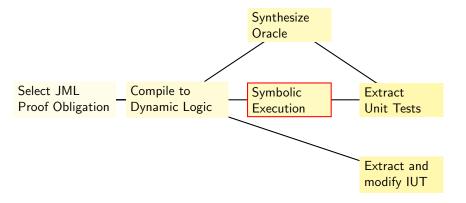
### **Program** *p*

```
_jmlresult8=Middle.middle(x,y,z);
```



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Verification-Based Test Generation Process**



# Extract IUT - Resulting Java Code

### State update ${\cal S}$ is translated into a sequence of assignments

```
_old0_x=x_lv_0;
_old1_y=y_lv_0;
_old2_z=z_lv_0;
x=x_lv_0;
y=y_lv_0;
z=z_lv_0;
_jmlresult8=Middle.middle(x,y,z);
```

Export program context, add getter and setter methods for private fields



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

# Symbolic Execution in Logic

Rules of Java Card DL Calculus that axiomatize program formulas implement symbolic execution

$$_{\text{IFELSE}} \frac{\Gamma, \ \mathcal{S}B \implies \mathcal{S} < \pi \ p \ \omega > \phi, \ \Delta \qquad \Gamma, \ !\mathcal{S}B \implies \mathcal{S} < \pi \ q \ \omega > \phi, \ \Delta}{\Gamma \implies \mathcal{S} < \pi \ \text{if} \ (B) \ \{ \ p \ \} \ \text{else} \ \{ \ q \ \} \ \omega > \phi, \ \Delta}$$

- ullet Branch conditions  $\mathcal{S}B$  and  $!\mathcal{S}B$  are added to the sequent
- $\bullet \ \ PC \ := \ \bigwedge_{\gamma \in \Gamma} \gamma \wedge \bigwedge_{\delta \in \Delta} \neg \delta \ \text{implies path condition of current path}$
- ullet If the execution path is infeasable PC is invalid and thus  $\Gamma \Rightarrow \Delta$  valid
- Interleave first-order deduction and symbolic execution





### **Example** (Finite Number of Execution Paths)

### Compute the middle of three numbers

```
public static int middle(int x, int y, int z){
    int mid = z;
    if (y < z)
        if (x<y){
            mid = y;
        else if(x < z)
            mid = x:
    }else{
        if (x>y)
            mid = y;
        else if(x>z)
            mid = x:
    return mid:
```

Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

# From Proof to Test, Cont'd

Test Generation

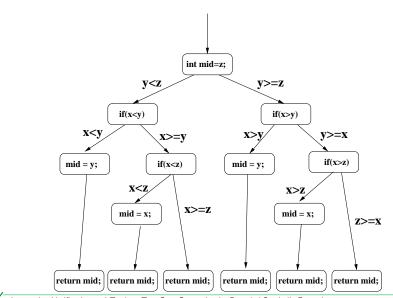
$$\mathsf{Pre} \Rightarrow \mathcal{S} \mathsf{<\!p\!>} \mathsf{Post}$$

- Attempt verification of PO, construct proof tree
- **2** Search for exit nodes  $\Gamma \Rightarrow S \Leftrightarrow \varphi$ ,  $\Delta$  in the proof tree Search for abnormally terminating paths  $\Gamma \Longrightarrow \mathcal{S} < \pi \text{ throw e;} \omega > \varphi, \Delta$
- 3 Collect accumulated path conditions at these points; weaken

$$PC := \bigwedge_{\gamma \in \Gamma} \gamma \wedge \bigwedge_{\delta \in \Delta} \neg \delta$$

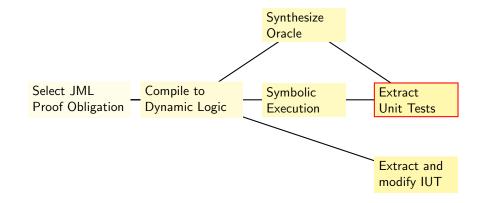
- Find first-order models of PC using, e.g., Simplify or Cogent Each model of each path condition yields a set of test data
- **5** Extract input variable assignment from found models

# Symbolic Execution Tree of middle()



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Verification-Based Test Generation Process**



### **Generated JUnit Test Case**

For every "feasible branch" in the proof tree one test method is generated



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

# **Bounded Symbolic Execution – Benefits and Shortcomings**

### Benefits

- Test generation remains automatic while a formal proof would need interaction
- In practice still a high code coverage

### Shortcomings

No guarantees on code coverage

### **Infinite Number of Execution Paths**

Code cannot be symbolically executed entirely without using induction or loop invariants

### Solution: Use Bounded Symbolic Execution

- Perform a bounded number of proof steps
- Unwind loops finite number of times, inline method bodies
- **3** Compute path conditions also for not yet terminated paths corresponding to leaves  $\Gamma \Longrightarrow \mathcal{S} < p' > \varphi$ ,  $\Delta$  of open branches in the proof tree



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### **Example** (Infinite Number of Execution Paths)

## **Determine Maximal Entry of Array**

```
/*@ public normal_behavior
  @ ensures (\forall int i;
  @ 0<=i && i<arr.length; arr[i]<=\result);
  @*/
public int getMax(int[] arr){
  int max = arr[0];
  for(int i=1; i<arr.length; i++){
    if(arr[i]<max) max = arr[i];
  }
  return max;
}</pre>
```





# **Example** (Infinite Number of Execution Paths)

**Example** (Infinite Number of Execution Paths)

Path conditions for execution paths through the loop body needed

### Loops are handled by bounded unwinding

$$\frac{\Gamma \Rightarrow \mathcal{S} < \pi \text{ l:if(c)} \{1': \{b'\} \text{ while(c)} \{b\}\} \ \omega > \varphi, \ \Delta}{\Gamma \Rightarrow \mathcal{S} < \pi \text{ while(c)} \{b\} \ \omega > \varphi, \ \Delta}$$



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

### Part IV

# **Integrating Testing and Verification**

- Why Integrate?
- Test-Case Generation by Bounded Symbolic Execution
- 15 Test-Case Generation from Method Specifications and Loop Invariants
- White-box testing by Combining Specification Extraction and Black-box testing
- **17** Proving Incorrectness of Programs

Oracles for quantified formulas are needed

### Quantified formulas in postcondition are evaluated using loops

```
(\forall int i; 0<=i && i<arr.length; arr[i]<=\result);
for(int i = 0; i<arr.length; i++){ ... }
Restrictions on the admissible quantification domain.</pre>
```



Integrating Verification and Testing: Test-Case Generation by Bounded Symbolic Execution

# Generating Tests from Loop Invariants and Method Specifications

### **Explicit execution of paths**

- Fully automatic
- Limited unwinding of loops and of recursion steps of methods

# Abstract/implicit execution of paths

- Requires (user provided) method specifications and loop invariants
- Full feasible branch coverage possible

# **Example 1: Branch after a Loop**

# void foo1(int n){ int i=0: while(i < n\*2){ n+=2: i+=8: **if**(i>=64){ C();

### Loop invariant rule (simplified)

```
\Gamma \Longrightarrow \{U\}I, \Delta
                                              \Gamma \Longrightarrow \{U\} \{M\} (I \land Ic \to [b] I), \Delta
                                            \Gamma \implies \{U\} \{M\} I \land \neg Ic \rightarrow [\pi \ \omega] \phi, \ \Delta
if(..){..}else{..}  \overline{\Gamma \Rightarrow \{U\} [\pi \text{ while(lc)}\{b\} \omega] \phi. \Delta}
```

### Result

Using the invariant:

KeY computed test data with: n = 33



Integrating Verification and Testing: ...from Method Specifications and Loop Invariants

# **Example 3: Branch after a Method Call**

```
class Foo{
 int i;
 void foo(int n){
                                         Method contract rule
    D(n):
                                         (simplified)
    if(i==20){ C(); }
 }
                                          \Gamma \Longrightarrow \{U\} \{T\} Pre, \Delta
                                          \Gamma \Longrightarrow \{U\} \{M \parallel T\} Post \rightarrow [\pi\omega] \phi, \Delta
 /*@ requires i<n;</pre>
                                         \Gamma \Rightarrow \{U\} [\pi \text{ m(t1,..,tn)}; \omega] \phi \Delta
    @ assignable i;
    @ ensures i==n; */
 void D(int n){ while(i<n)...}</pre>
```

# **Example 2: Branch within a Loop**

# void foo2(int n){ int i=0; while(i < n\*2){ n+=2: **if**(i>=64){ C(); i+=8;

### **Loop** invariant rule (simplified)

```
\Gamma \Longrightarrow \{U\}I, \Delta
 \Gamma \Longrightarrow \{U\} \{M\} (I \land Ic \to [b] I)\Delta
\Gamma \Longrightarrow \{U\} \{M\} I \land \neg Ic \to [\pi\omega] \phi, \Delta
\Gamma \Rightarrow \{U\} [\pi \text{ while(lc)}\{b\} \omega] \phi \Delta
```

### Result

Using the invariant:

$$\frac{4+i}{1+n-n_{pre}} = 4$$
We get:  $n = 34$ 

Integrating Verification and Testing: ... from Method Specifications and Loop Invariants

# More Generally

### **Testing Tasks**

- Branch after a loop
- Branch within a loop
- Branch after a method call

### (on Friday ...)

- How to compute the precondition more generally
- Required properties of must the specification or invariant

### Part IV

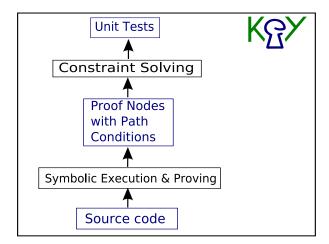
# **Integrating Testing and Verification**

- Why Integrate?
- 14 Test-Case Generation by Bounded Symbolic Execution
- **15** Test-Case Generation from Method Specifications and Loop Invariants
- **16** White-box testing by Combining Specification Extraction and Black-box testing
- **Proving Incorrectness of Programs**

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Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Previous Approach**



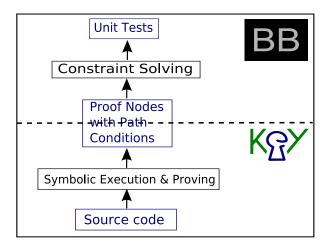
# **Next: Combining KeY with other Testing Tools**

KeY can extend Black-box Testing Tools



Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Next Appraoch**



## **Benefits**

- Using of existing Black-box Testing Tools for White-box testing
- Separation of concerns Modulariy
- Combination of Coverage Criteria

Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Tool Chain**

Requirement Specification

# Two Kinds of Specifications

### Requirement Specificaiton

- Given by the user
- Role: To be tested or verified

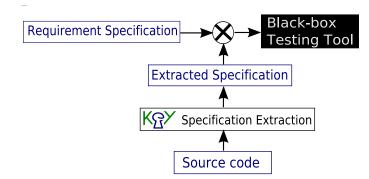
### **Extracted Specifictaion**

- Is extracted automatically
- Complies with the IUT by construction
- Reflects the structure of the program

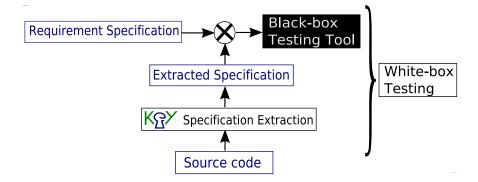


Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Tool Chain**



### **Tool Chain**



Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Specification Extraction**

### **Proof Obligation**

 $\Gamma$ , requires  $\rightarrow$  <absMin(a,b)> Dummy

### **Closed Proof Branches**

$$a = 1, a = 2 \Rightarrow \langle A... \rangle \phi \qquad a = 1, a \neq 2 \Rightarrow \langle B... \rangle \phi$$
$$a = 1 \Rightarrow \langle if(a==2) \{A\} \{B\}... \rangle \phi$$

# **Example IUT**

```
/*@ public normal_behavior
 @ ensures \result == ((a<b? a : b)<0 ?
                           -(a < b? a : b) :
                            (a<b? a : b)); @*/
  @
 public static int absMin ( int a, int b) {
    int result = b;
   if (a<b) { result=a; }</pre>
    if (result<0) { result=-result;}</pre>
   return result:
```

Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **Open Proof Branches**

for precondition 
$$\overline{a <= -1, b <= -1 + a} \Rightarrow \{ for postcondition \\ a >= 0, b <= -1 + a \Rightarrow \{ result := -a \} Dummy$$

$$b <= -1, a <= -1, b >= a \Rightarrow \{ result := -b \} Dummy$$

$$b >= 0, b >= a \Rightarrow \{ result := b \} Dummy$$

# **Combined Specification**

```
/*@ public normal_behavior
@ requires true;
@ ensures \result == ((a < b ? a : b) < 0 ?</pre>
                -((a < b ? a : b)):
                (a < b ? a : b)):
0
@ also
@ requires true && b <= -1 && a <= -1 && b >= a;
@ ensures \result == \old((b * -1)):
@ also
@ requires true && b >= 0 && b >= a;
@ ensures \result == \old(b);
@ also
@*/
```

Integrating Verification and Testing: Structure Extraction+Black-box=White-box

### **Black-box Tools**

### **Tool requirements**

- JML Support
- Derive tests based on method preconditions
- Ensure coverage of specification
- Generate tests automatically

# Specification Extraction + Black-box Testing = White-box Testing

Properties of this Approach



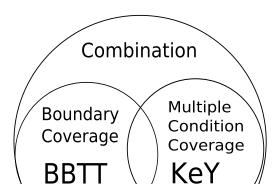
Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# **JML** supporting Black-box Tools

### BB-tools for JML

- JET / UTJML no coverage guarantees
- Korat based on class invariants
- JmlAutoTest implementation is lost
- JmITT specification animator, limited test generation support
- jmlunit generates only the oracle
- itest does not generate test data

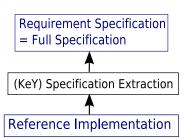
# **Combinations of Coverage Criteria**





Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# Requirement Specification from a Reference **Implementation**



# **Using the extracted Post Condition**

Requirement Specification

```
/*@ requires true;
    @ ensures \result!=23;
    absMin(int a, int b){...}

    With Full Specification

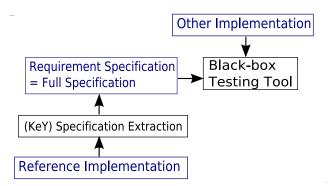
  /*@ requires true;
    @ ensures \result!=23;
    @ also...
    @ requires b >= 0 && b >= a;
    @ ensures \result == \old(b) @*/

    Simplified

 /*@ requires b >= 0 && b >= a;
    @ ensures 23 != \old(b);
    @ ... @*/
```

Integrating Verification and Testing: Structure Extraction+Black-box=White-box

# Requirement Specification from a Reference **Implementation**



### Part IV

# **Integrating Testing and Verification**

- **Test-Case Generation by Bounded Symbolic Execution**
- **Test-Case Generation from Method Specifications and Loop**
- White-box testing by Combining Specification Extraction and **Black-box testing**
- **11** Proving Incorrectness of Programs



Integrating Verification and Testing: Proving Incorrectness of Programs

# **Proving Incorrectness of Programs (2)**

### More generally, this covers:

- Reachability analysis: Can a program reach certain states?
- Inversion: Which pre-states lead to certain post-states? (i.e., construct models of the weakest pre-condition)
- Non-termination analysis (more details later)

# **Proving Incorrectness of Programs**

### Java DL can also directly express program incorrectness

- Basically: proving with negated input formula + quantification ⇒ Symbolic search for inputs that make post-condition fail
- Difference to generating test cases: Both path-constraints and post-condition are considered (Only "failing test cases" are found)
- Constraint solving by KeY itself
- Symbolic reasoning ⇒ "symbolic test cases" can be found

Integrating Verification and Testing: Proving Incorrectness of Programs

# **Example: Bug in Binary Search**

```
public int binSearch(int []ar, int target) {
  if (ar.length == 0) return -1;
  int hi = ar.length;
  int lo = 0:
  while (true) {
    int centre = (hi + lo) / 2:
    if (centre == lo) {
      if (ar[centre] == target) return centre;
      else if (ar[centre+1] == target) return centre+1;
      else return -1;
    if (ar[centre] < target) lo = centre;</pre>
    else if (target < ar[centre]) hi = centre;</pre>
    else return centre;
  } }
```





# **Example: Bug in Binary Search (2)**

Pre-condition: array is sorted:

$$ar \neq null \ \land \ \forall i : nat. \ (i < (ar.length - 1) \rightarrow ar[i] \leq ar[i + 1])$$

Post-conditions: result is desired index:

$$result \neq -1 \rightarrow (0 \leq result < ar.length \land ar[result] \doteq target)$$
  
 $result \doteq -1 \rightarrow \forall i : nat. (i < ar.length \rightarrow ar[i] \neq target)$ 

Dis-verification condition:

$$\exists pre\text{-state.} \neg (pre \rightarrow \langle binSearch(ar, target) \rangle post)$$



Integrating Verification and Testing: Proving Incorrectness of Programs

# Schema for Characterising Incorrectness in DL

$$\exists$$
 pre-state. {pre-state}  $\neg$  (pre-conditions  $\rightarrow$  \land program code \rangle post-conditions)

This formula holds if:

- pre-state satisfies the pre-conditions, and
- the program does not terminate, or
- terminates but violates the post-conditions.

Pre-state quantification has to cover:

- local variables, class attributes.
- instance attributes, arrays, number of allocated objects.

Technically:

• "Update" is needed for making pre-state active

# **Example: Bug in Binary Search (3)**

When proving the formula, KeY produces a constraint that describes critical inputs (automatically):

[ 
$$ar.length \doteq 1 \land ar[0] \neq target$$
 ]

Result: program behaves wrongly whenever

- the length of the given array is 1, and
- the searched number is not in the array.



Integrating Verification and Testing: Proving Incorrectness of Programs

# **Reasoning about Incorrectness Conditions**

```
∃ pre-state.
  \neg(pre-conditions \rightarrow \ program code \ post-conditions)
```

How to eliminate  $\exists pre-state$ ?

- In KeY: Metavariables + constraint solving
  - → Backtracking-free proving
  - $\rightarrow$  Systematic search for constraints that close a proof

### **Extension: Non-Termination Detection**

### Required: KeY + Invariant generator

As before, incorrectness can be expressed in DL:
 "Program p does not terminate for some pre-state"

$$\exists$$
 pre-state.  $\neg$ (pre-conditions  $\rightarrow$   $\langle$  program code  $\rangle$  true)

- In the proof, a non-termination invariant is required
  - ⇒ Program cannot reach terminal states
  - $\Rightarrow$  Invariant generator needed as extension to KeY
- Techniques to construct invariants in our approach:
  - ⇒ Invariant templates containing metavariables
  - $\Rightarrow$  Refinement based on failed proof attempts

(more information in the talk on Friday)



Integrating Verification and Testing: Proving Incorrectness of Programs

### Part V

# **Further Topics**

# **Example: Gaussian Sum**

### **KeY** + Invariant generator can prove non-termination automatically:

- Constraint on initial state: n < 0</li>
- Loop invariant (intermediate states): n < 0</li>
  - → Found in few iterations (2–4, depending on settings)



Integrating Verification and Testing: Proving Incorrectness of Programs

### Part V

# **Further Topics**

- 18 Taclets and Taclet Language
- **19** Correctness of Proof Rules
- **20** Dealing with Integers
- Proof Reuse
- **22** Concurrency

### **Taclets**

## Taclets are the "rules" of the KeY system

Taclets. . .

- have logical content like rules of the calculus
- have pragmatic information for interactive application
- have pragmatic information for automated application
- keep all these concerns separate but close to each other
- can easily be added to the system
- are given in a textual format
- can be verified w.r.t. base taclets



Integrating Verification and Testing: Taclets and Taclet Language

# An Axiom and a Branching Rule

### Closure rule

```
close_goal {
  \int (==> b)
  \assumes (b ==>)
  \closegoal
  \heuristics(closure)
};
```

### Cut rule

```
cut {
  \add (b ==>);
  \add (==> b)
};
```

# Taclet Syntax (by Example)

### Modus ponens: Rule

$$\frac{\Gamma, \ \phi, \ \psi \Longrightarrow \Delta}{\Gamma, \ \phi, \ \phi \Longrightarrow \Delta}$$

### Modus ponens: Taclet

```
modus_ponens{
  \find (phi -> psi ==>)
  \assumes (phi ==>)
  \replacewith (psi ==>)
  \heuristics(simplify)
```

Integrating Verification and Testing: Taclets and Taclet Language

# **Java Card Taclets**

# Rule if\_else\_split

$$B = \text{TRUE} \implies \langle \pi \ p \ \omega \rangle F$$

$$B = \text{FALSE} \implies \langle \pi \ q \ \omega \rangle F$$

$$\implies \langle \pi \ \text{if } (B) \ p \ \text{else} \ q \ \omega \rangle F$$

where B is a Boolean expression without side effects

### **Corresponding taclet**

```
if_else_split {
  \find (==> <{.. if(#B) #p else #q ...}>post)
  \replacewith (==> <{.. #p ...}>post) \add (#B = TRUE ==>);
  \replacewith (==> <{.. #q ...}>post) \add (#B = FALSE ==>)
  \heuristics(if_split)
};
```



# **Taclets: Summary**

### Taclets are ...

- simple and (sufficiently) powerful
- compact and clear notation
- no complicated meta-language
- easy to apply with a GUI
- validation possible

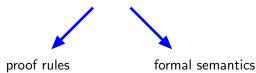


Integrating Verification and Testing: Taclets and Taclet Language

# **Verification Calculus Soundness**

# A fundamental problem!

informal language specification



### Part V

# **Further Topics**

- 18 Taclets and Taclet Language
- 19 Correctness of Proof Rules
- 20 Dealing with Integers
- 21 Proof Reuse
- 22 Concurrence



Integrating Verification and Testing: Correctness of Proof Rules

# **Validating Soundness of Proof Rules**

# **Bootstrapping**

Validate a core set of rules, generate and prove verification conditions for additional rules

### **Cross-verification**

 $\bullet$  against the  $B{\scriptscriptstyle ALI}$  calculus for Java formalized in Isabelle/HOL

[D. von Oheimb, T. Nipkow]

ullet against the Java semantics in the  $\mathrm{MAUDE}$  system

[J. Meseguer]

### Tests

Using the compiler test suite Jacks





# From the Java Language Specification

# PostIncrementExpression: PostfixExpression ++

At run time, if evaluation [...] completes abruptly, then the postfix increment expression completes abruptly and no incrementation occurs.

Otherwise, the value 1 is added to the value of the variable and the sum is stored back into the variable. Before the addition. binary numeric promotion is performed on the value [...] The value of the postfix increment expression is the value of the variable before the new value is stored.



Integrating Verification and Testing: Correctness of Proof Rules

# From the Jacks Conformance Test Suite

```
class T1241r1a {
    final int i=1; static final int j=1;
    static { }
}
class T1241r1b {
     /*@ public normal_behavior
       @ ensures \result == 7;  @ */
    public static int main() {
      int s = 0; T1241r1a a = null;
      s = s + a.j;
      try {s = s + a.i;}
      catch (Exception e) {
        s = s + 2; a = new T1241r1a();
        s = s + a.i + 3; }
      return s; }
```

### Rule for Postfix Increment

# Intuitive rule (not correct!)

$$\frac{\Rightarrow \langle \pi \ x=y; \ y=y+1; \ \omega \rangle \phi}{\Rightarrow \langle \pi \ x=y++; \ \omega \rangle \phi}$$

### But ...

$$x = 5 \implies \langle x = x + +; \rangle (x = 6)$$
 INVALID

### Correct rule

$$\frac{\Rightarrow \langle \pi \text{ v=y; y=y+1; x=v; } \omega \rangle \phi}{\Rightarrow \langle \pi \text{ x=y++; } \omega \rangle \phi}$$



Integrating Verification and Testing: Correctness of Proof Rules

### Part V

# **Further Topics**

- **Taclets and Taclet Language**
- Correctness of Proof Rules
- 20 Dealing with Integers
- 21 Proof Reuse

# **Specification of Integer Square Root**

### Taken from: Preliminary Design of JML [G. Leavens et al.]

```
/*0 requires y >= 0;
  @ ensures
  @ \result * \result <= y &&</pre>
  0 y < (abs(\result)+1) * (abs(\result)+1);</pre>
  @ */
 public static int isqrt(int y)
```

### But ...

\result = 1073741821 = 
$$\frac{max\_int-5}{2}$$
 satisfies spec for  $y=1$ .   
  $1073741821*1073741821 = -2147483639 \le 1$    
  $1073741822*1073741822 = 4 > 1$ 



Integrating Verification and Testing: Integers

# **Examples**

# Valid for Java integers

- $MAX_INT + 1 = MIN_INT$
- $MIN_INT * (-1) = MIN_INT$
- $\bullet \exists x, y. (x \neq 0 \land y \neq 0 \land x * y = 0)$

# Not valid for Java integers

•  $\forall x. \exists y. \ y > x$ 

# Not a sound rewrite rule for Java integers

• 
$$x + 1 > y + 1 \quad \rightsquigarrow \quad x > y$$

# Data Type Gap

# Specification level: Abstract data types

- Integer  $(\mathbb{Z})$
- Set, List

### Implementation level: Concrete programming language data types

- byte, short, int, long
- Array



Integrating Verification and Testing: Integers

# More Formal Semantics of Java Integer Types

# Range of primitive integer types in Java

Type	Range	Bits
byte	[-128, 127]	8
short	[-32768, 32767]	16
int	[-2147483648, 2147483647]	32
long	$[-2^{63}, 2^{63} - 1]$	64

# **Options for Integer Semantics Rules in KeY**

### Java semantics

- Faithfully axiomatises the overflow semantics of Java integers
- Leads to hard verification problems (lack of intuition)

### **Arithmetic semantics**

- Leads to easier verification problems
- Incorrect

# Arithmetic semantics with overflow check

- Correct
- Leads to moderate verification problems
- Incomplete (there are programs that are correct despite overflows)



Integrating Verification and Testing: Integers

# **Proof Reuse**

# Basic Use Case

- Verification attempt fails
- 2 Amend program
- Recycle unaffected proof parts

# **Example: Incremental Verification**

- Program correct w.r.t. arithmetic semantics?
- Program correct w.r.t. overflow checking semantics? X
- Fix bug, reuse proof 
  ✓

Successfully used in case studies

# Part V

# **Further Topics**

- Taclets and Taclet Language
- Correctness of Proof Rules
- 21 Proof Reuse



Integrating Verification and Testing: Proof Reuse

# **Proof Reuse**

### **Observations**

- Similar program rule applications focus on similar program parts
- Program rules applicable at a limited number of goals
- Proof structure follows program structure

# Steps

- 1 Identify changes in program (program diff)
- 2 Identify subproofs beginning with unaffected statements
- Similarity-guided proof replay

# Part V

# **Further Topics**

- **18** Taclets and Taclet Language
- 20 Dealing with Integers
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- **22** Concurrency



Integrating Verification and Testing: Concurrency

# java.lang.StringBuffer

```
private char value[];
private int count;
public synchronized StringBuffer
                        append(char c) {
    int newcount = count + 1;
   if (newcount > value.length)
        expandCapacity(newcount);
    value[count++] = c;
    return this;
```

# Verifying concurrent Java programs

Full reasoning about data

Beyond just safety or race detection

No abstractions



Integrating Verification and Testing: Concurrency

# Verify That...

strb. = 
$$0 \land \neg \text{strb} = \text{null} \land \text{strb.count} = 0 \rightarrow \forall n. \ n > 0 \rightarrow$$

$$<^{\{n\}} \text{strb.append(c);}^{\{0\}} > \text{strb.count} = n \land$$

$$\forall k. \ 0 \le k < n \rightarrow \text{strb.value}[k] = \text{c}(p_1(k+1))$$

# **Three-Step Programme**

**Statistics** 

Unfold

Prove atomicity invariant

3 Symbolic execution + induction



Integrating Verification and Testing: Concurrency

# **Concurrency Verification Problems**

- Number of threads
  - ⇒ symmetry reduction (this work)
- Number of interference points
  - ⇒ exploit locking, data confinement
- Java Memory Model

• Proof steps: 14622

• Branches: 238 (3 relevant)

• Interactions: 2

■ Runtime: ~1 minute

• Result: conjecture false for  $n \ge MAX\_INT$ 



Integrating Verification and Testing: Concurrency

# Alas...

No thread identities in programs

No dynamic thread creation (but unbounded concurrency)

Currently only atomic loops

# The Calculus Is Built On...

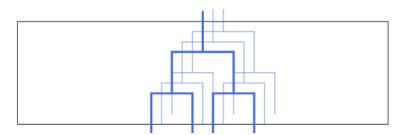
# symmetry reduction

...and explicit scheduler formalization

Integrating Verification and Testing: Concurrency

# The Calculus Is Built On...

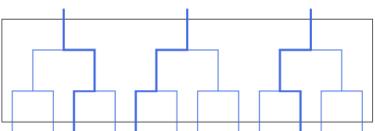
# symmetry reduction



...and explicit scheduler formalization

# The Calculus Is Built On...

# symmetry reduction



...and explicit scheduler formalization

Integrating Verification and Testing: Concurrency

Part VI

Wrap Up

# Part VI

# Wrap Up

- Case Studies
- **24** Current Directions of Work
- **25** Acknowledgments



Integrating Verification and Testing: Case Studies

# **Algorithm Verification**

# **Schorr-Waite Algorithm**

- Graph-marking algorithm (memory-efficient garbage collection)
- Very complicated loop invariant
- One single proof with 17,000 steps

# "Fundamental" Case Studies: Libraries

# Java Collections Framework (JCF)

- Part of JCF (treating sets) specified using UML/OCL
- Parts of reference implementation verified

# Java Card API Reference Implementation

- Covers whole of latest API used in practice (2.2.1)
- 60 classes, 4,500 lines of Java code
- Effort: 2–3 (expert) months



Integrating Verification and Testing: Case Studies

# Security Case Studies: Java Card Software

### Demoney

• Electronic purse application provided by Trusted Logic S.A.

### Mondex Card

- Smart card for electronic financial transactions
- Issued by NatWest in 1996
- Proposed as case study in Grand Challenge
- KeY used to verify a reference implementation in Java Card

# **Safety Case Study**

### **Avionics Software**

- Java implementation of a Flight Manager module at Thales Avionics
- Comprehensive specification using JML, emphasis on class invariants
- Verification of some nested method calls using contracts

# Virtual Machine for Real Time Secury Java

• Verification of some library functions of the Jamaica VM from Aicas



Integrating Verification and Testing: Case Studies

# Some Current Directions of Research in KeY

- Multi-threaded Java
- Integration of deduction and static analysis
- Integration of verification and testing
- Counter examples
- Symbolic error propagation
- Verification of MISRA C
- Proof visualization, proving as debugging

Extension of dynamic logic for multi-threading Symbolic execution calculus Prototype available, StringBuffer class verified

## Part VI

# Wrap Up

- Case Studies
- **24** Current Directions of Work
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Integrating Verification and Testing: Current Directions of Work

# Some Current Directions of Research in KeY

- Multi-threaded Java
- Integration of deduction and static analysis
- Integration of verification and testing
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- Symbolic error propagation
- Verification of MISRA C
- Proof visualization, proving as debugging

Mutual call of analyser/prover, common semantic framework Implementation of static analysis in theorem proving frame

# Some Current Directions of Research in KeY

- Multi-threaded Java
- Integration of deduction and static analysis
- Integration of verification and testing
- Counter examples
- Symbolic error propagation
- Verification of MISRA C
- Proof visualization, proving as debugging

Generation of test cases from proofs Symbolic testing New coverage criteria



Integrating Verification and Testing: Current Directions of Work

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- Multi-threaded Java
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- Integration of verification and testing
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- Symbolic error propagation
- Verification of MISRA C
- Proof visualization, proving as debugging

Symbolic error classes modeled by formulas Error injection by instrumentation of Java Card DL rules Symbolic error propagation via symbolic execution

# Some Current Directions of Research in KeY

- Multi-threaded Java
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- Verification of MISRA C
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Generate counter example from failed proof attempt Counter example search as proof of uncorrectness



Integrating Verification and Testing: Current Directions of Work

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Integrating Verification and Testing: Current Directions of Work

# Part VI

# Wrap Up

- **Current Directions of Work**
- **Acknowledgments**

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Integrating Verification and Testing: Current Directions of Work

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Integrating Verification and Testing: Acknowledgments

# **More Information**

# The KeY Book

B. Beckert, R. Hähnle, P. H. Schmitt (eds.)

Verification of Object-Oriented Software: The KeY Approach

Springer-Verlag, LNCS 4334, 2007.



### Web site

# www.key-project.org



### Integrating Verification and Testing: More Information

# Part VI

# Wrap Up

- Case Studies
- **24** Current Directions of Work
- **25** Acknowledgments



Integrating Verification and Testing: More Information